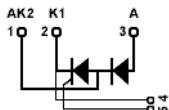


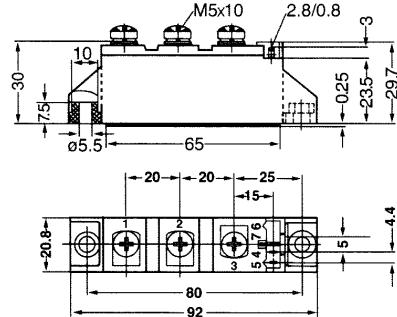
# CTD90, CDT90

## Thyristor-Diode Modules, Diode-Thyristor Modules



Type	V <sub>RSM</sub> V <sub>DSDM</sub> V	V <sub>RRM</sub> V <sub>DRM</sub> V
CTD/CDT90GK08	900	800
CTD/CDT90GK12	1300	1200
CTD/CDT90GK14	1500	1400
CTD/CDT90GK16	1700	1600
CTD/CDT90GK18	1900	1800
CTD/CDT90GK20	2100	2000

Dimensions in mm (1mm=0.0394")



Symbol	Test Conditions	Maximum Ratings	Unit
I <sub>TRMS</sub> , I <sub>FRMS</sub> I <sub>TAVM</sub> , I <sub>FAVM</sub>	T <sub>VJ</sub> =T <sub>VJM</sub> T <sub>C</sub> =85°C; 180° sine	180 90	A
I <sub>TSM</sub> , I <sub>FSM</sub>	T <sub>VJ</sub> =45°C V <sub>R</sub> =0 t=10ms (50Hz), sine t=8.3ms (60Hz), sine	1700 1800	A
	T <sub>VJ</sub> =T <sub>VJM</sub> V <sub>R</sub> =0 t=10ms(50Hz), sine t=8.3ms(60Hz), sine	1540 1640	
$\int i^2 dt$	T <sub>VJ</sub> =45°C V <sub>R</sub> =0 t=10ms (50Hz), sine t=8.3ms (60Hz), sine	14450 13500	A <sup>2</sup> s
	T <sub>VJ</sub> =T <sub>VJM</sub> V <sub>R</sub> =0 t=10ms(50Hz), sine t=8.3ms(60Hz), sine	11850 11300	
(di/dt) <sub>cr</sub>	T <sub>VJ</sub> =T <sub>VJM</sub> f=50Hz, t <sub>p</sub> =200us V <sub>D</sub> =2/3V <sub>DRM</sub> I <sub>G</sub> =0.45A dI <sub>G</sub> /dt=0.45A/us	150 500	A/us
(dv/dt) <sub>cr</sub>	T <sub>VJ</sub> =T <sub>VJM</sub> ; V <sub>DR</sub> =2/3V <sub>DRM</sub> R <sub>GK</sub> =∞; method 1 (linear voltage rise)	1000	V/us
P <sub>GM</sub>	T <sub>VJ</sub> =T <sub>VJM</sub> t <sub>p</sub> =30us I <sub>T</sub> =I <sub>TAVM</sub> t <sub>p</sub> =300us	10 5	W
P <sub>GAV</sub>		0.5	W
V <sub>RGM</sub>		10	V
T <sub>VJ</sub> T <sub>VJM</sub> T <sub>stg</sub>		-40...+125 125 -40...+125	°C
V <sub>ISOL</sub>	50/60Hz, RMS I <sub>ISOL</sub> ≤1mA	3000 3600	V~
M <sub>d</sub>	Mounting torque (M5) Terminal connection torque (M5)	2.5-4.0/22-35 2.5-4.0/22-35	Nm/lb.in.
Weight	Typical including screws	90	g

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# CTD90, CDT90

## Thyristor-Diode Modules, Diode-Thyristor Modules

Symbol	Test Conditions	Characteristic Values	Unit
$I_{RRM}, I_{DRM}$	$T_{VJ}=T_{VJM}; V_R=V_{RRM}; V_D=V_{DRM}$	5	mA
$V_T, V_F$	$I_T, I_F=300A; T_{VJ}=25^\circ C$	1.74	V
$V_{TO}$	For power-loss calculations only ( $T_{VJ}=125^\circ C$ )	0.85	V
$r_T$		3.2	$m\Omega$
$V_{GT}$	$V_D=6V; T_{VJ}=25^\circ C$ $T_{VJ}=-40^\circ C$	2.5 2.6	V
$I_{GT}$	$V_D=6V; T_{VJ}=25^\circ C$ $T_{VJ}=-40^\circ C$	150 200	mA
$V_{GD}$	$T_{VJ}=T_{VJM}; V_D=2/3V_{DRM}$	0.2	V
$I_{GD}$		10	mA
$I_L$	$T_{VJ}=25^\circ C; t_p=10\mu s; V_D=6V$ $I_G=0.45A; dI/dt=0.45A/\mu s$	450	mA
$I_H$	$T_{VJ}=25^\circ C; V_D=6V; R_{GK}=\infty$	200	mA
$t_{gd}$	$T_{VJ}=25^\circ C; V_D=1/2V_{DRM}$ $I_G=0.45A; dI/dt=0.45A/\mu s$	2	us
$t_q$	$T_{VJ}=T_{VJM}; I_T=150A; t_p=200\mu s; -dI/dt=10A/\mu s$ $V_R=100V; dv/dt=20V/\mu s; V_D=2/3V_{DRM}$	typ. 185	us
$Q_s$	$T_{VJ}=T_{VJM}; I_T, I_F=50A; -dI/dt=6A/\mu s$	170	uC
$I_{RM}$		45	A
$R_{thJC}$	per thyristor/diode; DC current per module	0.3 0.15	K/W
$R_{thJK}$	per thyristor/diode; DC current per module	0.5 0.25	K/W
$ds$	Creeping distance on surface	12.7	mm
$da$	Strike distance through air	9.6	mm
$a$	Maximum allowable acceleration	50	$m/s^2$

### FEATURES

- \* International standard package
- \* Direct copper bonded Al<sub>2</sub>O<sub>3</sub>-ceramic base plate
- \* Planar passivated chips
- \* Isolation voltage 3600 V~
- \* UL registered, E 72873
- \* Gate-cathode twin pins for version 1

### APPLICATIONS

- \* DC motor control
- \* Softstart AC motor controller
- \* Light, heat and temperature control

### ADVANTAGES

- \* Space and weight savings
- \* Simple mounting with two screws
- \* Improved temperature and power cycling
- \* Reduced protection circuits

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# CTD90, CDT90

## Thyristor-Diode Modules, Diode-Thyristor Modules

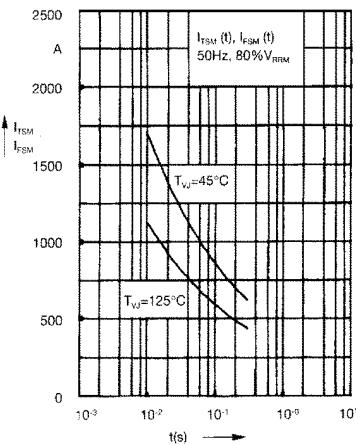


Fig. 1 Surge overload current  
 $I_{TSM}, I_{FSM}$ : Crest value,  $t$ : duration

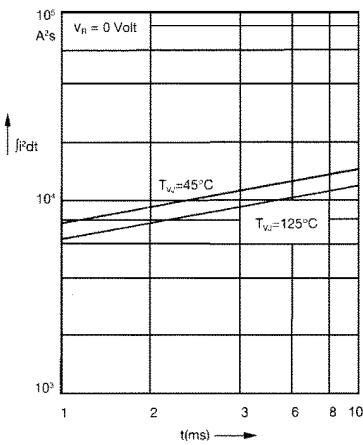


Fig. 2  $\int I^2 dt$  versus time (1-10 ms)

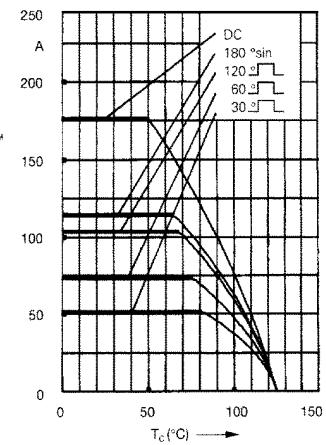


Fig. 2a Maximum forward current at case temperature

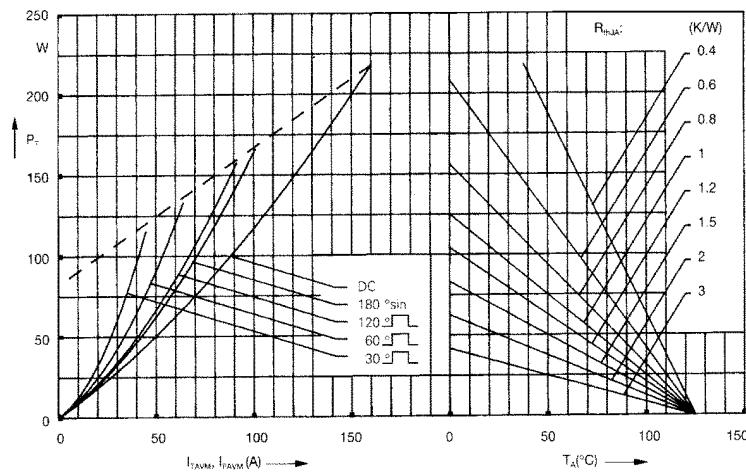


Fig. 3 Power dissipation versus on-state current and ambient temperature (per thyristor or diode)

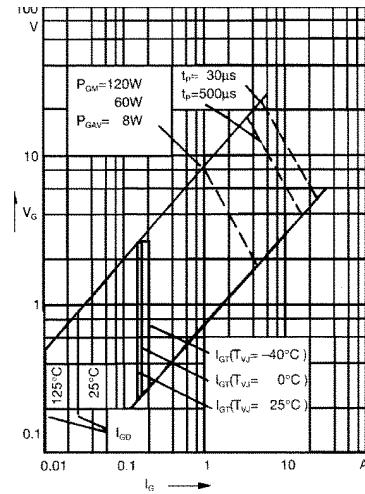


Fig. 4 Gate trigger characteristics

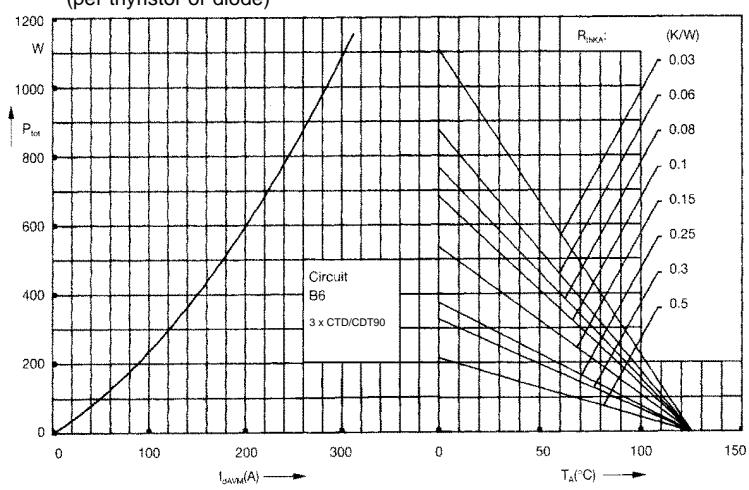


Fig. 5 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature

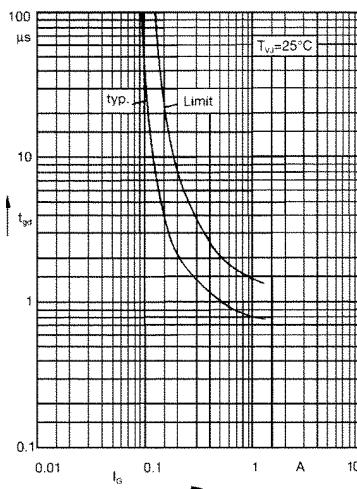


Fig. 6 Gate trigger delay time

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# CTD90, CDT90

## Thyristor-Diode Modules, Diode-Thyristor Modules

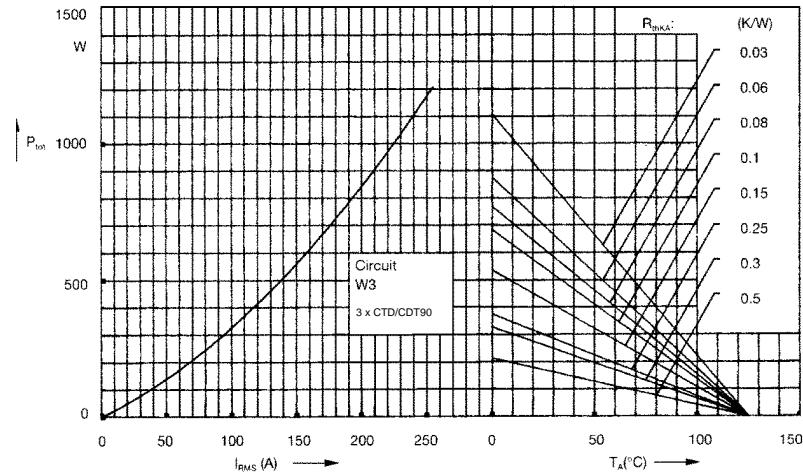


Fig. 7 Three phase AC-controller:  
Power dissipation versus RMS  
output current and ambient  
temperature

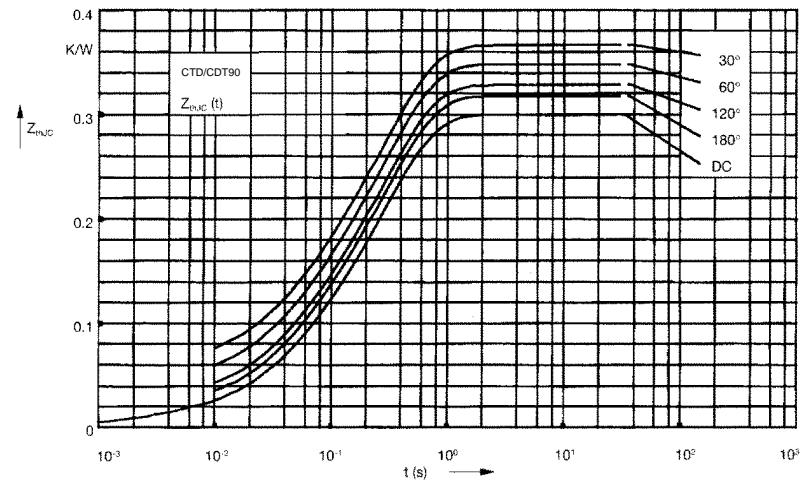


Fig. 8 Transient thermal impedance  
junction to case (per thyristor or  
diode)

$R_{\text{thJC}}$  for various conduction angles d:

d	$R_{\text{thJC}}$ (K/W)
DC	0.3
180°C	0.31
120°C	0.33
60°C	0.35
30°C	0.37

Constants for  $Z_{\text{thJC}}$  calculation:

i	$R_{\text{thi}}$ (K/W)	$t_i$ (s)
1	0.008	0.0019
2	0.054	0.047
3	0.238	0.3

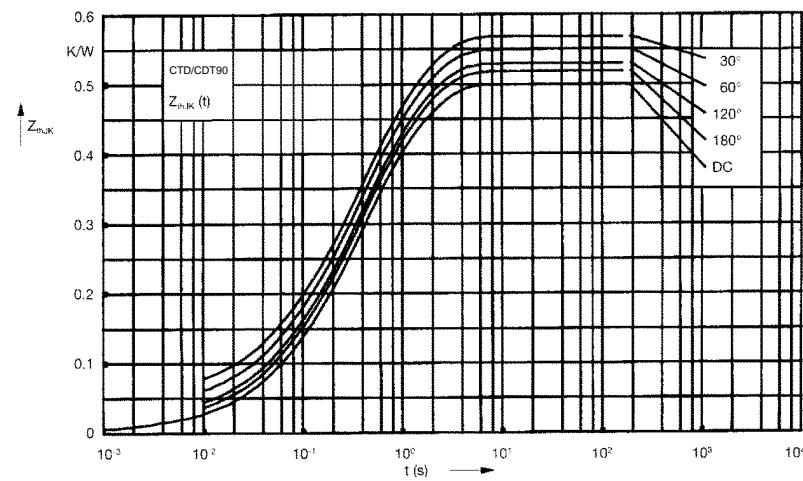


Fig. 9 Transient thermal impedance  
junction to heatsink (per thyristor or  
diode)

$R_{\text{thJK}}$  for various conduction angles d:

d	$R_{\text{thJK}}$ (K/W)
DC	0.5
180°C	0.51
120°C	0.53
60°C	0.55
30°C	0.57

Constants for  $Z_{\text{thJK}}$  calculation:

i	$R_{\text{thi}}$ (K/W)	$t_i$ (s)
1	0.008	0.0019
2	0.054	0.047
3	0.238	0.3
4	0.2	1.25

D E E C o r p .