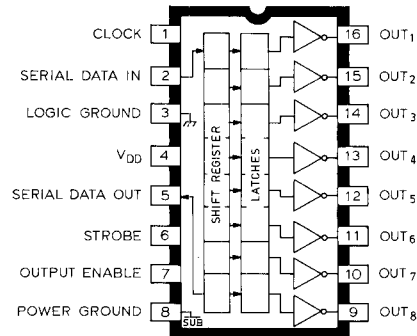


BIMOS SERIAL-INPUT, LATCHED SINK DRIVERS UCN5821A THRU UCN5824A, UCN5829EB, AND UCN5841A/EP/LW THRU UCN5843A/EP/LW

FEATURES

- 3.3 MHz Minimum Data Input Rate
- CMOS, PMOS, NMOS, TTL Compatible
- Low-Power CMOS Logic and Latches
- High-Voltage Current Sink Outputs
- For the UCN5829EB only:
 - Internal Current Sensing
 - Constant-Frequency PWM Current Control
 - Control For External High-Side Driver
 - User-Defined Output Enable Timeout
 - Internal Thermal Shutdown Circuitry

UCN5821A thru UCN5824A

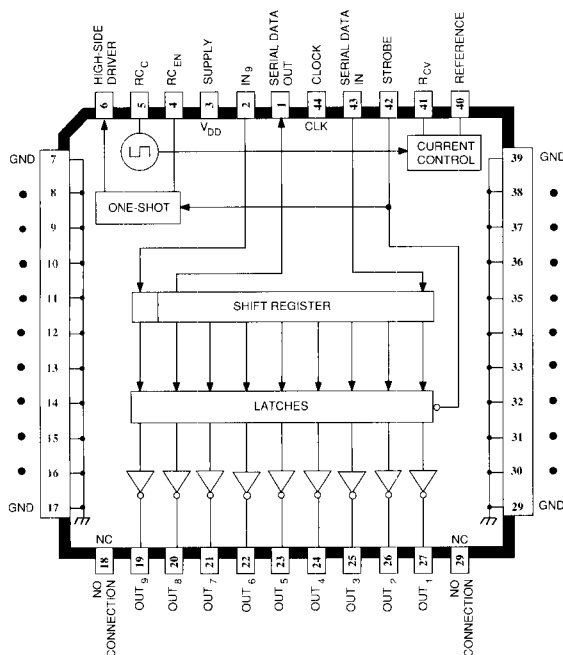


Dwg. PP-026

Part Number	Serial-Input Shift Register	Output Ratings	Output Features
UCN5821A UCN5822A UCN5823A	8-Bit	50 V, 350 mA 80 V, 350 mA 100 V, 350 mA	Darlington
UCN5824A	8-Bit	50 V, 100 mA	Saturated
UCN5829EB	9-Bit	50 V, 1.6 A	PWM Current Control
UCN5841A/EP/LW UCN5842A/EP/LW UCN5843A/EP/LW	8-Bit	35 V, 350 mA 50 V, 350 mA 50 V, 350 mA	Internal Diodes

Suffix A = DIP, EB = Batwing PLCC, EP = PLCC, LW = wide-body SOIC

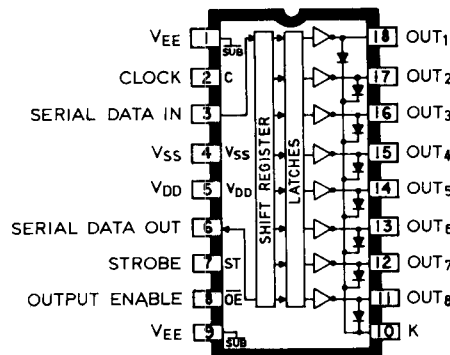
UCN5829EB



Dwg. PP-028

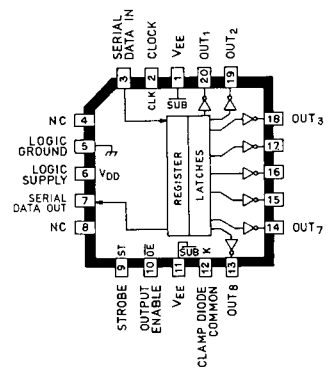
UCN5841LW thru UCN5843LW

UCN5841A thru UCN5843A



Dwg. No. A-12,659

UCN5841EP thru UCN5843EP



Dwg. No. A-14,362